

SEMICONDUCTOR PROCESSING PROCESS CONTROL SYSTEM
AND ITS CONTROL METHOD

RELATED APPLICATION

5 This application claims the benefit of priority under 35 U.S.C. § 119 of Japanese Patent Application No. H11-186523, filed on June 30, 1999, the entire contents of which are incorporated by reference herein.

10 BACKGROUND OF THE INVENTION

Field of the Invention

This invention relates to a system and a method for semiconductor processing process control, and more particularly, to a semiconductor processing process control system capable of
15 flexibly, quickly coping with changes in processing process, method of computing control variables and processing equipment, and its control method.

Description of the Related Background Art

For manufacturing semiconductor devices such as IC, LSI,
20 and so on, they undergo many processes like etching, cleansing, inspection, and others, and intended processing is conducted in each process. For realizing intended processing, control conditions each processing apparatus and its processing time must be adjusted appropriately.

25 For example, in case of stacking a film in a certain process, material of the film (film material) and its thickness are targets of the processing. The content of this processing is to make a film of an intended material on a specific semiconductor. Only when control conditions of the deposition apparatus (in case of
30 a CVD apparatus, the are conditions on the material of the gas, flow rate of the gas, temperature, etc.) are selected properly, the intended content of the processing (making a film of an intended material) can be realized, and the film thickness can be controlled by adjusting the duration of time of the deposition (deposition
35 time). The deposition time can be obtained by dividing the target film thickness by the deposition rate (film thickness stacked in a unit time, namely, a kind of processing speed). Such

deposition rates are previously measured upon maintenance of the apparatus, for example, and held in form of a rate table.

When etching processing is conducted, the content of the processing is to etch a film of a specific material stacked on a specific semiconductor, and this processing content and the etching depth (film thickness) are the target of the processing. To realize this content of the processing, control conditions of an etching apparatus are determined. Further, the etching time (processing time) is determined by dividing the target etching depth (thickness) by the etching rate (etching depth in each unit time, i.e., a kind of processing speed). Such etching rates are held as a rate table similarly to the case of deposition of films.

To supervise control conditions of processing apparatuses and processing times in a plurality of processes, a semiconductor processing process control system is used. Taking here deposition of a film (as a single process) as an example of processing content, a flow of process control by a conventional semiconductor processing process control system is shown. The process flow is shown in Fig. 29, and the flow of process control is shown in Fig. 30.

As shown in Fig. 30, a semiconductor processing process control system CS1 reads out the processing target (in this case, material and thickness of the film to be stacked) of the process and control conditions of a processing apparatus from process flow information IF1. Then, the semiconductor processing process control system CS1 obtains the processing time (in this case, deposition time) by referring to a rate table RT1 and dividing the target film thickness by the deposition rate. In this example, 100 minutes obtained by dividing 1000 angstrom as the target film thickness by 10 angstrom/min. as the deposition rate is the deposition time. Then, it sends the control variables (setting of conditions concerning control of the processing apparatus involving both control conditions of the processing apparatus and its processing time, also in the description made hereunder) to the processing apparatus PA1 like a deposition apparatus or its control device, for example. In the example shown in Fig. 30, the control method for the target process is a fixed method.

Figs. 31 and 32 show method of computing the processing time upon stacking a plurality of films and the processing time upon etching a plurality of films. In this case, since control conditions and processing speed (deposition rate, etching rate, etc.) of the processing apparatus PA1 vary with materials of films to be stacked or etched, processing time must be calculated for each step for processing each film.

The semiconductor processing process control system CS1 may be made up of hardware alone. Normally, however, it can be made as programs on a computer (software) to ensure quick response to changes in process, etc. This is shown in Fig. 33.

As shown in Fig. 33, there are process control programs CPG for respective processing apparatuses PA1 through PA4 used for respective processes, and these programs control the processing apparatuses PA1 through PA4. That is, a set of a plurality of process control programs CPG realizes the function of the semiconductor processing process control system CS1. When the process and the apparatus therefor are changed from one to another, the system can cope with it by changing the content of the respective process control programs CPG.

However, there is the problem that control conditions are not fixed ones determined only by processing apparatuses PA1 through PA4 and processing contents and deposition rate is not always constant.

That is, control conditions are variable with the history of use of each processing apparatus. Additionally, deposition rate may change depending on the status of the base on which a target film should be stacked. Therefore, it is possible that the deposition rate varies with thickness of the stacked film.

As a method dealing with this problem, Japanese Patent Laid-Open Publication No. hei 8-45804 discloses a method for computing appropriate control conditions taking the past control conditions into consideration, and Japanese Patent Laid-Open Publication No. hei 6-196404 discloses a method for computing a processing time from a target film thickness.

These methods for computing control conditions and processing time are not always fixed but may vary with target

processing contents and other factors. Conventional semiconductor processing process control systems, however, could not flexibly cope with changes in calculation method of control conditions and processing time, and were compelled to re-make the whole program every time upon changing the calculation method.

Furthermore, conventional systems involved the problem that they needed a change of the whole program also upon every change of the processing apparatus and process (not only a change of the process from one to another, but also omission and addition of a step).

As explained above, conventional semiconductor processing process control systems could not quickly cope with changes in process, calculation method of control variables, processing apparatus, and so on, and as a result, there was the possibility of delaying development of semiconductor devices.

SUMMARY OF THE INVENTION

It is therefore an object of the invention to provide a semiconductor processing process control system which can flexibly, quickly cope with changes in process, calculation method of control variables and processing apparatus.

In order to accomplish the aforementioned and other objects, according to one aspect of the present invention, a semiconductor processing process control system comprises:

a process controller main body having a function of controlling part of controls of processes for semiconductor processing, which is independent from semiconductor processing devices and processing targets; and

a plurality of control variable computing sections provided for respective the semiconductor processing devices and the processing targets, and detachably inserted into to the process controller main body to obtain control variables of semiconductor processing devices which meet the semiconductor processing devices and the processing targets, the process controller main body controlling processes of semiconductor processing on the basis of the control variables obtained by the control variable computing section inserted therein.

According to another aspect of the present invention, a semiconductor processing process control system comprises:

5 a process controller main body having a function of controlling part of controls of processes for semiconductor processing, which is independent from semiconductor processing devices and processing targets;

10 a plurality of control variable computing sections provided for respective the semiconductor processing devices and the processing targets to obtain control variables of semiconductor processing devices which meet the semiconductor processing devices and the processing targets; and

15 a control variable computation method section detachably inserted into the process controller main body to manage the control variable computing sections according to a computing method over a plurality of predetermined processes, the control variable computing sections detachably inserted into the control variable computation method section, the process controller main body controlling processes of semiconductor processing on the basis of the control variables obtained by the control variable computation method section inserted into the process controller main body and the control variable computing section inserted into the control variable computation method section.

25 According to a further aspect of the present invention, a semiconductor processing process control system for controlling a plurality of processes for semiconductor processing, comprises:

30 a skip judgment request receiving section which receives a request for judgment whether a process can be skipped or not; a plurality of judgment plug-in each having a step skip judgment logic for judging whether a certain process can be skipped or not;

a skip judgment yes/no section for searching out a judgment plug-in corresponding to a process to be judged, which is received by the skip judgment request receiving section, from the plurality of judgment plug-in;

35 a judgment execute section which activates the judgment plug-in searched out by the skip judgment yes/no section and makes the judgment plug-in judge whether the one process can be skipped

or not, on the basis of the step skip judgment logic;

a judgment result receiving section which receives from the activated judgment plug-in a result of judgment whether the one process can be skipped or not; and

- 5 a skip execute section which effects skipping of the one process when the result of judgment indicates that the process can be skipped.

According to one aspect of the invention, a method for controlling a semiconductor processing process control system which includes a process controller main body having the function of controlling part of controls of processes for semiconductor processing, which is independent from semiconductor processing devices and processing targets, comprises the steps of:

- 10 selecting one of a plurality of control variable computing sections which are provided for respective the semiconductor processing devices and the processing targets and can be detachably inserted into the process controller main body, and inserting the control variable computing section, which is selected, into the process controller main body;

- 15 operating the control variable computing section inserted into the process controller main body to obtain a control variable of a semiconductor processing device, which is agreeable with the semiconductor processing devices and the processing targets; and

- 20 operating the process controller main body to control a process for semiconductor processing on the basis of the control variable obtained.

According to another aspect of the invention, a method for controlling a semiconductor processing process control system which includes a process controller main body having the function of controlling part of controls of processes for semiconductor processing, which is independent from semiconductor processing devices and processing targets, comprises the steps of:

- 30 selecting at least one of a plurality of control variable computing sections, which are provided for respective the semiconductor processing devices and the processing targets;

selecting one of a plurality control variable computation

method sections which manage the control variable computing sections over a plurality of processes, the control variable computation method sections being configured to be detachably inserted into the process controller main body when necessary, and the control variable computing sections being configured to be detachably inserted into the control variable computation method sections;

inserting the control variable computing sections selected into the control variable computation method section selected, and inserting the control variable computation method section into the process controller main body;

operating the control variable computation method section inserted into the process controller main body and the control variable computing section inserted into the control variable computation method section to obtain a control variable of a semiconductor processing device which is agreeable with the semiconductor processing devices and the processing targets; and

operating the process controller main body to control processes for semiconductor processing on the basis of the control variable obtained.

According to a further aspect of the invention, a method for controlling a semiconductor processing process control system configured to control a plurality of processes for semiconductor processing, comprises:

a skip judgment request receiving step for receiving a request for judgment whether a process can be skipped or not;

a search step for searching out a judgment plug-in corresponding to a process to be judged, which is received by the skip judgment request received in the skip judgment request receiving step from a plurality of judgment plug-in, each the judgment plug-in having a step skip judgment logic for judging whether a certain process can be skipped or not;

a judgment execute step for activating the judgment plug-in searched out to judge whether the one process can be skipped or not;

a judgment result receiving step for receiving from the activated judgment plug-in a result of judgment whether the one

process can be skipped or not; and

a skip execute step which effects skipping of the one step when the result of judgment indicates that the process can be skipped.

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BRIEF DESCRIPTION OF THE DRAWINGS.

Fig. 1A is a block diagram showing configuration of a semiconductor processing process control system and its peripheral portion according to the first embodiment of the invention;

10 Fig. 1B is a diagram showing a process flow and a control variable computation program in the first embodiment of the invention;

Fig. 2 is a diagram showing a program module according to the first embodiment of the invention;

15 Fig. 3 is a diagram showing a process flow of semiconductor processing;

Fig. 4 is a diagram showing a hardware configuration according to the first embodiment of the invention;

20 Fig. 5A is a block diagram of the first example of control variable computation program according to the first embodiment;

Fig. 5B is a diagram showing the concept of the first example of control variable computation program according to the first embodiment;

25 Fig. 6 is a block diagram showing the second example of control variable computation program according to the first embodiment;

Fig. 7 is a block diagram showing details of a process controller main body according to the invention;

30 Fig. 8 is a diagram showing a flow of processing in the process controller main body according to the invention;

Fig. 9 is a diagram showing a flow of data during practical use of the invention;

Fig. 10 is a diagram showing a process flow upon computing processing time in a film deposition process using the invention;

35 Fig. 11A is a block diagram showing configuration of a semiconductor processing process control system and its peripheral portion according to the second embodiment of the invention;

Fig. 11B is a diagram showing a process flow of semiconductor processing in case of a control over a plurality of processes;

Fig. 12A is a block diagram showing details of the main body of control variables according to the second embodiment of the invention;

Fig. 12B is a block diagram schematically showing the main body of control variables according to the second embodiment of the invention;

Fig. 13 is a diagram showing a flow of data according to the second embodiment of the invention;

Fig. 14 is a diagram showing an example of process information and physical step information according to the third embodiment of the invention;

Fig. 15 is a diagram showing an example of computation of processing time according to the third embodiment of the invention;

Fig. 16 is a diagram showing a flow of a process according to the fourth embodiment of the invention;

Fig. 17 is a diagram showing a flow of data according to the fourth embodiment of the invention;

Fig. 18 is a diagram showing an example of computer program according to the fourth embodiment of the invention;

Fig. 19 is a diagram showing a functional block according to the fifth embodiment of the invention;

Fig. 20 is a diagram showing hardware configuration according to the fifth embodiment of the invention;

Fig. 21 is a block diagram showing a functional block according to the fifth embodiment of the invention;

Fig. 22 is a diagram showing operations of a skip judgment request receiver in the fifth embodiment of the invention;

Fig. 23 is a diagram showing operations of a target process skip judgment Yes/No section in the fifth embodiment of the invention;

Fig. 24 is a diagram showing a judgment logic of judgment plug-in in the fifth embodiment of the invention;

Fig. 25 is a diagram showing operations of a spec search section in the fifth embodiment of the invention;

Fig. 26 is a diagram showing operations of a target process

QC result extractor in the fifth embodiment of the invention;

Fig. 27 is a diagram showing operations of a target process skip judging section and a skip execute section in the fifth embodiment of the invention;

5 Fig. 28 is a diagram which shows an example of operations in case of recording the history of skip judgment as know-how data and supplying information to an external device;

Fig. 29 is a diagram showing a process flow in a conventional system;

10 Fig. 30 is a diagram showing a flow of process control in a conventional system;

Fig. 31 is a diagram showing a conventional processing time computation method used for stacking a plurality of films;

15 Fig. 32 is a diagram showing a conventional processing time computation method used for etching a plurality of films; and

Fig. 33 is a diagram showing a conventional concept of configuration of a semiconductor processing process control system.

20 DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

(First Embodiment)

The first embodiment of the invention is a semiconductor processing process control system, which is made up of a process controller main body and control variable computation programs and configured to use the control variable computation programs by changing them from one to another depending upon the process. This is explained below in greater detail.

30 Fig. 1A is a block diagram showing configuration of this embodiment. Main body 100 of a process controller is coupled to a control variable computing section 200 having a plurality of control variable computation programs 210. Each of the control variable computation programs 210 constitutes the control variable computing section in this embodiment.

35 The process controller main body 100 is connected to a process manager 300 and one or more semiconductor producing devices 400 and/or semiconductor inspectors 402. The semiconductor producing devices 400 and the semiconductor inspectors 402 constitute the

semiconductor processing apparatus in this embodiment.

The process manager 300 is a super system computer which supervises the entire process flow, and it sends process flow information to the process controller main body 100. This process
 5 flow information is also called context information, and indicates the order of processes to be executed by the semiconductor producing devices 400 and the semiconductor inspectors 402 and contents of target processing in respective processes.

The process controller main body 100 is a section independent
 10 from the semiconductor producing devices 400, semiconductor inspectors 402 and recipes. That is, the process controller main body 100 has the function of executing a part not depending upon the semiconductor processing apparatus and the processing target among controls of processes of semiconductor processing. Control
 15 variable computation programs 210(1) through 210(n) form a computing section for computing control variables of the processing apparatus. A feature of this embodiment lies in that any of the control variable computation programs 210(1) through 210(n) prepared for respective recipes can be select and plugged
 20 into the process controller main body 100. That is, the control variable computation programs 210(1) through 210(n) are configured to be loaded in and unloaded from the process controller main body 100. A recipe herein means is processing conditions for conducting a certain processing of a certain product in a certain
 25 apparatus. Even when a common recipe is used, if the product is different and the recipe is used in a different position and order in a process, the control variable computation program 210 may vary.

Fig. 1B is a diagram showing the process flow and the control
 30 variable computation program 210 in the first embodiment of the invention. The control variable computation program 210(i) is applied to a process i. Function of the control variable computation program 210 can be described in forma of a program. Its specific example is shown in Fig. 2. Since the entire process
 35 flow of semiconductor processing also includes an inspection process in addition to the processing (manufacturing) process as shown in Fig. 3, let a "processing" used here involve inspection

as well, and if a certain process is an inspection process, let the control variable computation program 210(i) be used by appropriately replacing one with another also in the inspection process.

5 Fig. 4 is a diagram showing a hardware configuration of the semiconductor processing process control system according to the first embodiment of the invention. As shown in Fig. 4, the semiconductor processing process control system includes a process control main server 510, process control sub-server 512,
10 QC (quality control) data server 520 and process managing server 530 and device control servers 540(1) through 540(n) which are connected via a network.

 Connected to the process control server 510, QC data server 520 and process managing server 530 are auxiliary storage devices,
15 515 and 516, which establish database therein. In this case, the process controller main body 100 and the control variable computation program 210 of Fig. 1 correspond to the process control main server 510, and the process manager 300 corresponds to the process managing server 530. Controllers of the semiconductor
20 producing device 400 and the semiconductor inspector 402 are connected to the device control server 540.

 As shown in Figs. 5A and 5B, the control variable computation program 210 is divided into a computation manager 210A for managing the flow of procedures and processing of computing control
25 variables and a formula section 210B composed of computation formulas used for computation of control variables. Since the computation manager 210A varies with device and recipe, it exists for each device and each recipe. The formula section 210B is capable of description inside the control variable computation
30 program 210 in this manner, but it can also introduce and use a formula of an external application (linkage to an external program).

 On the other hand, as shown in Fig. 6, the control variable computation program 210 can be separated into a control calculation
35 program 211 exclusive to calculation of pure control variables of the processing devices, and an actual processing totaling program 212 exclusive to treatment of processing status data of

processing devices, temporary storage and calculation of device constants of processing devices based on these data. Even in this case, the control computation program 211 and the actual processing totaling program 212 can be divided into computation managers
 5 211A, 212A and formula sections 211B, 212B.

Next shown in Fig. 7 are details of the process controller main body 100. As shown in Fig. 7, the process controller main body 100 includes a process judging section 110, flow information acquiring section 120, control variable sender/receiver 130,
 10 control computation select/execute section 140, and temporary data holder 150.

The process judging section 110 is coupled to the flow information acquiring section 120, control variable sender/receiver 130 and control computation select/execute
 15 section 140. The control computation select section 140 is further coupled to the control variable sender/receiver 130 and the temporary data holder 150. Thus, the entirety constructs the process controller main body 100. The control computation select/execute section 140 can select and plug in desired one
 20 of control variable computation programs 210(1) through 210(n).

Selection of control variable computation programs 210(1) through 210(n) is conducted by the process controller main body 100. A flow chart of this processing is shown in Fig. 8.

Registered in the process flow information are target
 25 devices to be used for processing or information for determining such target devices and recipes to be used by target devices or information for determining such recipes. Therefore, as shown in Fig. 8, the process judging section 110 acquires this process information from the flow information acquiring section 120 and
 30 obtains the name of a target device and the name of a recipe (step 10). Subsequently, in the control computation select/execute section 140, by using a device recipe managing table, which manages devices and recipes, a control variable computation program 210 corresponding to the acquired names of the device and the recipe
 35 is searched (step S11). That is, the device recipe managing table manages relations between combinations of names of groups of devices, names of devices and names of recipes and corresponding

control computation programs.

Next judged is whether a corresponding control variable computation program 210 exists or not as a result of the search (step S12). When a corresponding control variable computation
 5 program 210 exists, this control variable computation program 210 is read out onto a memory, and parameters necessary for the computation are exchanged to execute the control variable computation program (step S13).

When computation by the control variable computation
 10 program is finished, judgment is made whether the control variable computation program 210 has been finished normally or not (step S14). If it has been finished normally, the result of the computation is output to the control variable sender/receiver 130, and this processing is finished. Even when no corresponding
 15 control variable computation program 210 corresponding exists in step S12, or the control variable computation program 210 was not finished property in step S14, this processing is finished.

Next referring to the data flow diagram of Fig. 9 describing details of the above-explained flow, contents of the processing
 20 by the semiconductor processing process control system is explained in greater detail. In Fig. 9, the control variable computation program 210 is divided into the control computation program 211 and the actual processing totaling program 212 as shown in Fig. 6.

25 First, process flow information is sent from the process manager 300 to the flow information acquiring section 120 of the process controller main body 100. The flow information acquiring section 120 sends this process flow information to the process judging section 110. Based on this process flow information, the
 30 process judging section 110 judges the processing status, process, device, and so on. Information obtained by the judgment (status of the processing, such as the start of the processing, apparatus, and so forth) is delivered to the control computation select/execute section 140.

35 Based on the process information and information about the apparatus, status of the processing, etc., the control computation select/execute section 140 selects and starts a control

computation program 211. The started control computation program 211 refers to data in the temporary data holder 150 and various data in the QC database 232, and executes computation. The computation result obtained by the control computation program

5 211 is sent to the control computation select/execute section 140. Then, after being replaced into control parameters agreeable to respective devices by the control variable sender/receiver 130, it is sent to the semiconductor producing device 400 (or semiconductor inspector 402).

10 In the above processing, if the process judging section 110 judges that the status of the processing is completion of the processing or inspection, the control computation select/execute section 140 starts the actual processing totaling program 212 for totaling actual processing. It is previously

15 described in this actual processing totaling program 212 that a certain kind of processing data (for example, thickness of a film, actual processing data) from the device, for example.

In this manner, the control variable sender/receiver 130 receives processing data from the semiconductor producing device

20 400 (or semiconductor inspector 402). This processing data is sent to the control computation select/execute section 140. Further, if necessary, the flow information acquiring section 120 acquires process flow information from the process manager 300. This process flow information contains process managing

25 information. Therefore, the flow information acquiring section 120 extracts the process managing information from the process flow information, and sends it to the control computation select/execute section 140.

The control computation select/execute section 140 delivers

30 to the actual processing totaling program 212 necessary information among the processing data and the process managing information. The actual processing totaling program 212 acquires respective processing data and apparatus constants from the QC database 232, if necessary. Then, based on the received

35 information, the actual processing totaling program 212 totals necessary data processing and its results, and stores them in the temporary data holder 150. The processing data stored in the

temporary data holder 150 is adequately used in the control computation program 211 as explained above.

Next referring to Fig. 10, a flow of processing by the present embodiment is explained by way of a specific example. Fig. 10 is a diagram showing a process flow upon computing processing time in a film deposition process. Based on the process information acquired from the process flow information, the control computation select/execute section 140 selects one of control variable computation programs 210. Contents of the processing in the control variable computation program 210, in this case, are: (1) reading the target film thickness from the process flow information; (2) determining control variables from the apparatus (name of the apparatus) and content of the processing (name of the recipe); (3) computing the deposition time using a formula; and so forth. Computation results obtained are downloaded to the controller of the semiconductor producing device 400 or the semiconductor inspector 402 via the control variable sender/receiver 130.

As explained above, since the semiconductor processing process control system of this embodiment is divided to the process controller main body 100 which controls the process for semiconductor processing independently from the semiconductor producing device 400 and the semiconductor inspector 402, semiconductor producing device 400 and/or semiconductor inspector 402, and control variable computation program 210 for obtaining control variables meeting with the target processing to be executed thereby, and uses a required control variable computation program 210 by plugging it to the process controller main body 100, the system can readily cope with changes even upon changes in semiconductor producing device 400 and/or semiconductor inspector 402 and intended processing.

For example, even when the target of processing by a single semiconductor producing device 400, it is sufficient to modify only a control variable computation program 210 corresponding to the semiconductor producing device 400, without influences to the other semiconductor producing devices 400 and semiconductor inspectors 402. Therefore, the system can be changed without

interrupting operations of the other semiconductor producing devices 4 and semiconductor inspectors 402.

(Second Embodiment)

The second embodiment of the invention constructs the
 5 control variable computing section from a control variable
 computation method program such that the control variable
 computation method program appropriately activates a plurality
 of control variable computation programs connected thereto to
 enable control of a plurality of steps. This is explained below
 10 in greater detail.

Fig. 11A show blocks of components of this embodiment. The
 control variable computation method program 220 is plugged into
 the process controller main body 100, and a plurality of control
 variable computation programs 210 are plugged into the control
 15 variable computation method program 220. The control variable
 computation programs 210 are prepared for respective processes.
 The control variable computation method program 220 forms a control
 variable computation method section in this embodiment.

The process controller main body 100 and the control variable
 20 computation program 210 have similar functions to those of the
 first embodiment, but this embodiment is different in that the
 control variable computation method program 220 is added between
 them. The control variable computation method program 220 has
 the function of managing the control variable computation programs
 25 210 in order to perform a process control over a plurality of
 processes.

An example of operations of this embodiment is shown in
 Fig. 11B. In this example, control variable computation over three
 processes of process I, process j and process k is conducted,
 30 and the control variable computation method program 220 is in
 charge of management of information related among these three
 processes and appropriately activation of the control variable
 computation programs 210(n, I), 210(n, j), 210(n, k). In this
 manner, control variable computation programs 210 corresponding
 35 to respective ones of a plurality of processes can be managed
 collectively by the control variable computation method program
 220. The control variable computation method program 220 is

characterized in being capable of describing a method of calculation over a plurality of processes in addition to binding and management of a plurality of control variable computation programs 210. Similarly to the first embodiment, the term
 5 "processing" is used here to involve inspection as well.

As shown in Figs. 12A and 12B, each control variable computation programs 210 is divided into a computation manager 210A for managing the flow of procedures and processing of computing control variables and a formula section 210B composed of
 10 computation formulas used for computation of control variables similarly to the first embodiment. The control variable computation method program 220 contains a plural steps computation method section 221 describing a method of computation over a plurality of processes. If the computation method is different,
 15 another control variable computation method program 220 replacing it can be used.

In this embodiment, configuration other than the control variable computation method program 220 is the same as the first embodiment, and explanation about the other configuration is
 20 omitted.

A flow of entire data in this embodiment is shown in Fig. 13. Here again like the first embodiment, each control variable computation program 210 is separated to a control computation program 211 and an actual processing totaling program 212.

25 A flow of the processing in this embodiment is explained below. Process flow information is acquired by the flow information acquiring section 120 and delivered to the control computation select/execute section 140 through the process judging section 110. Then, based on instructions from the control
 30 computation select/execute section 140, the control variable computation method program 220 selects a control variable computation program 210(α), and activates it appropriately. At that time, the control variable computation method program 220 selects the control variable computation program 210(α) in
 35 accordance with the process flow information acquired from the process flow information.

The control variable computation program 210(α) executes

the same processing as that in the first embodiment explained with reference to Fig. 9 to obtain a result of computation from the control variable computation program 210(α) and deliver control parameters to the semiconductor producing device 400A.

5 The result of the computation by the control variable computation program 210(α) is also stored in the temporary data holder 150.

The processing data obtained from the semiconductor producing device 400A is sent to the actual processing totaling program 212(α). The actual processing totaling program 212(α)
10 totals the processing data and stores its computation result in the temporary data holder 150.

Subsequently, in the same manner, the control variable computation method program 220 selects the control variable computation program 210(β), and activates it appropriately. The
15 processing content in the control variable computation program 210(β) is to acquire data as a result of inspection of the film thickness from the semiconductor inspector 402 and hold it in the temporary data holder 150.

In the next process, the control variable computation method
20 program 220 selects and activates the control variable computation program 210(γ). In this example, the control variable computation program 210(γ) is designed to conduct predetermined computation and obtain the etching time from inspection data of the film thickness held in the temporary data holder 150 and the
25 latest device constant acquired from the AC data base (in this case, the processing speed, especially the etching rate). This computation result is sent as control variables to controllers of the semiconductor producing device 400 and the semiconductor inspector 402 through the control variable sender/receiver 130.

30 At that time, the control variable computation method program 220 activates the control variable computation program 210(β) and the control variable computation program 210(γ), and makes a relation between their data. More specifically, the control variable computation method program 220 sends common data
35 information to the temporary data holder 150 and manages the temporary data holder 150. Further, the control variable computation method program 220 start control information to the

control variable computation program 210(β) and the control variable computation program 210(γ) via a program start managing section 234. The start control information is information for exclusively activating each program.

5 As explained above, since the semiconductor processing process control system according to this embodiment is so configured that control variable computation method programs 220 can be inserted into and removed from the process controller main body 100 and control variable computation programs 210 can be
10 inserted into and removed from the control variable computation method programs 220, control over a plurality of processes can be conducted easily.

 Additionally, like the first embodiment already explained, since the semiconductor processing process control system of this
15 embodiment is divided to the process controller main body 100 which controls the process for semiconductor processing independently from the semiconductor producing device 400 and/or the semiconductor inspector 402, semiconductor producing device 400 and/or semiconductor inspector 402, and control variable
20 computation program 210 and control variable computation method program 220 for obtaining control variables meeting with the target processing to be executed thereby, and uses a required control variable computation program 210 by plugging it to the process controller main body 100 via the control variable computation
25 method program 220, the system can readily cope with changes even upon changes in semiconductor producing device 400, semiconductor inspector 402 and intended processing.

(Third Example)

 The third embodiment of the invention is a semiconductor
30 processing process control system including therein a correspondence information data section indicating correspondence between logical step processing and physical step processing, and it is capable of having each physical step processing related to a logical step.

35 Configuration of this embodiment is the same as, in particular, the second embodiment excepting the use of the correspondence information data section.

Explained below is an example in which etching of a plurality of films is conducted in this embodiment. Fig. 14 shows process flow information and physical step information (information about combinations of control parameters in individual physical steps), and Fig. 15 shows a method of computing processing time.

As shown in Figs. 14 and 15, the process flow information has a description of processes for simultaneously etching a film A (of a material A1 having a thickness A2) and a film B (of a material B1 having a thickness B2) to be processed under a condition D and for etching a film C (of a material C1 having a thickness C2) under a condition E. That is, a process for sequentially etching films under two different conditions within a single process is described in the process flow information.

These two steps directly connected to respective processing conditions at that time are called logical steps. That is, in this case, simultaneous etching of the film A and the film B is one logical step, and etching of the film C is another logical step. These two logical steps are realized by executing four physical steps adding a step for stabilization of the processing before each etching process.

By referring to the logical/physical step managing table provided in the correspondence information data section, it is known that the first logical step and control condition D correspond to the second physical step whereas the second logical step and control condition E correspond to the fourth physical step. On the basis of these relations, by referring to a root table inside the QC database 232 (see Fig. 9), etching rates are acquired. Then, processing times F, G for the first logical step and the second logical step are calculated from the target thicknesses of films (depths) and etching rates. This formula is incorporated into the control variable computation program 210 and used for computation. The calculated processing time F is the processing time of the second physical step, and the processing time G is the processing time of the third physical step.

As explained above, according to the semiconductor processing process control system of this embodiment, it is possible to establish correspondence between logical steps shown

in the process flow information and physical steps concerning actual operations of semiconductor producing devices 400 and/or semiconductor inspectors 402. Therefore, semiconductor producing devices 400 and semiconductor inspectors 402 can be controlled reliably.

(Fourth Embodiment)

The fourth embodiment of the invention is modified from the second embodiment such that the actual processing computation program judges whether a process may be omitted or not.

10 A process flow for concretely explaining this embodiment is shown in Fig. 16, a flow of data is shown in Fig. 17, and a computer program used therefore is shown in Fig. 18.

In the example of Fig. 16, let the process includes three processes of a CVD deposition process, cleansing process and dust examining process. Although the cleansing process is used for removing dust generated in the deposition process, assume here that judgment about omission of the cleansing process is conducted when the amount of dust detected in the dust examining process is small. That is, when the totaling result by the actual processing computation program 212(ε) shows that the amount of dust remains below a predetermined reference value for a predetermined period, the cleansing process is omitted. That is, the cleansing process is skipped.

As shown in Fig. 17, process flow information (names of apparatuses, names of recipes, processing times) of the deposition process, cleansing process and dust examining process are acquired by the flow information acquiring section 120 (see Fig. 7), then sent to the control computation select/execute section 140 (see Fig. 7), and an appropriate control variable computation method program 220 is selected. The control variable computation method program 220 activates the control variable computation program 210(δ) of the cleansing process. As a result, cleansing is executed in the semiconductor producing device 400. Data of this processing is totaled by the actual processing totaling program 212(δ). This actual processing totaling program 212(δ) acquires data on the amount of dust of the preceding lot held in the temporary data holder 150, and it judges omission or execution of the process

according to a formula appropriately preset for judgment about omission of the process.

In the next process, the control variable computation method program 220 activates the control variable computation program 210(ε). As a result, dust inspection is conducted by the semiconductor inspector 402. Data of this processing is totaled by the actual processing totaling program 212(ε). That is, the actual processing totaling program 212(ε) acquires data about the mount of dust from the semiconductor inspector 402 which is a dust inspector. Subsequently, the actual processing totaling program 212(ε) holds the data about the amount of dust in the temporary data holder 150. The data on the amount of dust is used in the next lot for judging whether the cleansing step should be omitted or not.

In this manner, totaling operations of these two actual processing totaling programs 212(δ) and 212(ε) are executed by the control variable computation method program 220. This control variable computation method program 220 is in charge of management of the temporary data holder 150 and delivery of the start instruction to the actual processing totaling programs 212 through a program start management functioning section 234, among others.

As explained above, since the semiconductor processing process control system according to this embodiment is configured to total results of inspection in the inspection process and automatically judge, in response to the result of the inspection, whether the cleansing process precedent to the inspection process can be omitted or not, judgment about omission or execution of a cleansing process heretofore made by human operators can be done automatically by the system. Therefore, it is possible to progress systemization of controls of processes for semiconductor processing and hence reduce the term of works and the manufacturing cost.

(Fifth Embodiment)

The fifth embodiment of the invention is a step skip device in which a step skip judging section for judgment about skipping of a step is provided as an external plug-in which can be easily detached. This embodiment is explained below in detail.

A function block of this embodiment is shown in Fig. 19, and a computer (hardware) for realizing this function is shown in Fig. 20. A step skip device 70 includes a skip judgment request receiver 71, skip judgment Yes/No section 72, judgment execute section 73, judgment result receiver 74, skip execute section 75, judgment result register section 76, skip condition database 77 and know-how database 78. A detachable judgment plug-in 80 is coupled to the step skip device 70. Further, an external system 79 is connected to the step skip device 70 via the know-how database 78.

Operations in this embodiment are explained below in sequence by way of a specific example. The skip judgment request receiver 71 receives a skip judgment request by lot process start information. In response to the request, the skip judgment yes/no section 72 searches into the skip condition database 77 in which types of semiconductor devices to be manufactured, names of judgment plug-in used for skip judgment of a process, among others, are registered, and extracts a judgment plug-in 80 corresponding to the process.

In receipt of the extracted information, the judgment execute section 73 activates the corresponding judgment plug-in 80 to have it execute judgment whether step skip should be done or not. Result of the judgment by the judgment plug-in 80 is received by the judgment result receiver 74, and step skip is executed by the skip execute section 75. The judgment result register section 76 records the history of skip judgment in the know-how database 78 to enable supply of information to the external system 79.

Next referring to Fig. 20, hardware configuration of the step skip device according to the embodiment is explained. The step skip device according to this embodiment includes a step skip judging device 90, step progress terminal 91, process management database 92, QC database 93, and external system 79 which are connected via a network.

The step skip judging device 90 includes CPU 90a, RAM 90b, local disc 90c, cache database 90d and know-how database 78.

CPU 90a executes step skip judging programs and programs

of the judgment plug-in 80. In RAM 90b, step skip judging programs and programs of the judging plug-in 80 are stored. In the local disc 90c, OS and various programs are stored. In the cache database 90d, QC results are stored temporarily. The know-how database 78 stores results of judgment whether a process should be skipped or not. The process management database 92 stores process management information. The QC database 93 stores QC data.

As explained above, since the semiconductor processing process control system according to this embodiment is so configured that the judgment plug-in 80 can be inserted into or removed from the step skip device 70, it can readily cope with changes in judgment logic.

(Sixth Embodiment)

The sixth embodiment of the invention is so configured that a detachable judgment plug-in is in charge of acquiring QC (quality control) data and judgment about step skip based on the data.

Fig. 21 is a diagram showing functional blocks of a step skip device and a judgment plug-in according to the embodiment. As shown in Fig. 21, the judgment plug-in 80 includes a spec database 81, spec detector 82, QC result extractor 83, skip judge 84 and cache database 90d. The QC result extractor 83 is supplied with information from the QC (quality control) database 93.

Next based on Figs. 22 through 27 and referring to Fig. 21, operations in this embodiment are explained in sequence by way of a specific example. Figs. 22 through 27 are diagrams showing an example of concrete processing by the embodiment.

As shown in Fig. 22, the skip judgment request receiver 71 receives a skip judgment request by lot processing start information. In this example, it is judged whether a process β of a semiconductor product AAAA should be skipped or not.

Next as shown in Fig. 23, in response to this request, the skip judgment yes/no section 72 searches into the skip condition database 77 in which types of semiconductor devices to be manufactured, names of judgment plug-in used for the skip judgment of a process, among others, are registered, and extracts a judgment plug-in 80 corresponding to the process. This skip condition database 77 holds registration of types of semiconductor products,

skip judgment yes/no information (information about whether skip judgment itself should be done or not) for respective processes, and names of logics (names of judgment logic-in) used for skip judgment, and the judgment plug-in 80 is searched out by the name
 5 of the judgment plug-in. In this example, the name of the judgment plug-in is SAKURA.

After that, the judgment execute section 73 starts up SAKURA from the corresponding judgment plug-in 80. At that time, the judgment logic of the judgment plug-in 80 has no numerical values
 10 in judgment specs D, E, F and G as shown in Fig. 24. Therefore, as shown in Fig. 25, the spec detector 82 acquires judgment specs as references of skip judgment from the spec database 81 on the basis of the input information, and substitutes them for the judgment logic. In this example, if the thickness of the film
 15 to be judged is in the range of 1000 through 1100 angstrom four consecutive times, skipping of the process is approved.

Next as shown in Fig. 26, the QC result extractor 83 acquires prior quality control information about the target process from the QC (quality control) database 93, and registers (stores) it
 20 in the cache database 85.

Next as shown in Fig. 27, the skip judging section 84 judges from data in the cache database 85 whether the target process should be skipped or not, and sends its judgment result to the judgment result receiver 74. In receipt of it, the skip execute
 25 section 75 executes skipping of the process. That is, in this example, the process β (for example, cleansing process) is skipped.

It is also possible to configure the system to cancel skipping of this step β . That is, in the inspection process which is the
 30 process γ next to the process β , if an amount of dust beyond a predetermined reference range is detected, it is possible not to skip the cleansing process as the process β .

History of execution of skip judgment by the judgment result register section 76 is recorded in the know-how database 78. Then
 35 the history is supplied to the external system 90 via the know-how database 78. An example of use of this history is shown in Fig. 28. In this example, a lot schedule managing system 90a is shown

as the external system 90. The lot schedule managing system 90a executes re-scheduling on the basis of the history in the know-how database 78. That is, by skipping the process β , the number of steps of the entire process flow is reduced. Therefore, the delivery time of the products can be shortened. In the example of Fig. 28, the delivery period, XX days, can be shortened to YY days. In this manner, by using history information in the know-how database 78, high-accuracy process schedule management is possible.

As explained above, since the semiconductor processing process control system according to the embodiment is so configured that the judgment plug-in 80 acquires the past quality information from the QC database 93 and judges from a judgment spec in the spec database 81 whether the past quality information satisfies the condition for step skip, it is possible to accurately judge whether a process should be skipped or not, while ensuring a high reliability of the finished result. As a result, the term of works and the lot manufacturing cost can be reduced.

Further, since the judgment plug-in 80 is detachable relative to the step skip device 70, the logic for judging the process to be skipped or not from the finished result can be provided from outside. Therefore, logics for judgment can be modified and added flexibly.

The invention can be modified in various modes, not limited to the above-explained embodiments. For example, at least one of the control computation program 211 and the actual processing totaling program 212 of the control variable computation program 210 shown in Fig. 6 can be omitted, if it is not necessary.

Furthermore, the above-explained processing can be distributed by recording procedures necessary for each processing on a recording medium, which can be read out by a computer. In this case, by using a computer to read out and execute programs recorded on the recording medium, the semiconductor processing process control system according to the invention can be realized.

As described above, according to the invention, since the semiconductor processing process control system is constructed by dividing it into the process controller main body for controlling

processes for semiconductor processing independently from semiconductor processing devices and processing targets, and the control variable computing section for obtaining a semiconductor processing device and control variables of the semiconductor processing device which meet a processing target, and by configuring the control variable computing section to be detachably coupled to the process controller main body, the system can flexibly, quickly cope with changes in required process, formula for calculation of control variables and required processing device. Therefore, the production line can be activated early for producing a small amount of many types of semiconductor devices.

Furthermore, since the control variable computation method section is configured to be detachably connected to the process controller main body and to detachably receive the control variable computation means inserted therein, control over a plurality of processes can be executed, separating it into respective processes. Therefore, even upon the need for a new operation mode or modification among processes, the required modification can be made easily, and automation of control variable computation in the production line of semiconductor devices can be made quickly.